



International Workshop on Applied Reconfigurable Computing (ARC 2006)

Call for Papers

<http://www.arc-workshop.org>

Delft, The Netherlands, March 1-2-3, 2006

The workshop is sponsored by the IEEE Benelux Computer Society

IMPORTANT NOTICE

Due to the printing process associated with the LNCS-series, the proceedings will be produced and mailed to all participants AFTER the workshop.

We have therefore changed the 'Important Dates' part as well as the number of pages

Reconfigurable computing technologies offer the promise of substantial performance gains over traditional architectures via the customizing, even at run-time, the topology of the underlying architecture to match the specific needs of a given application. Contemporary configurable architectures allow for the definition of architectures with functional and storage units that match in function, bit-width and control structures the specific needs of a given computation. The flexibility enabled by reconfiguration is also seen as a basic technique for overcoming transient failures in emerging device structures.

The ARC forum therefore aims at bringing together researchers and practitioners of reconfigurable computing with an emphasis on practical applications of this promising technology. This year's workshop will have a series of invited speakers from leading industrial companies that will express their company's or industry's view on the future of reconfigurable technology.

The workshop will be held on the campus of Delft University of Technology in Delft, The Netherlands, on March 1-2-3, 2006. (*It precedes Date2006 by 2 days only and there are daily flights from Amsterdam to Munich*)

General Chairs

- Joao Cardoso, Univ. of Algarve, Portugal
- Stamatis Vassiliadis, Delft University of Technology, Netherlands

Program Chair

- Koen Bertels, Delft University of Technology, Netherlands

Cyber Chair

- Elena Moscu Panainte, Delft University of Technology, Netherlands

Program Committee (not complete)

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- Russell Tessier, University of Massachusetts, USA
- Stephan Wong, Delft Univ. of Technology, the Netherlands
- Tim Callahan, Carnegie Mellon University, USA
- Wayne Luke, Imperial College, UK
- Andy Pimentel, University of Amsterdam, the Netherlands
- Pedro Trancoso, University of Cyprus, Cyprus

Workshop Secretariat

- Lidwina Tromp, Delft University of Technology

Website

www.arc-workshop.org/arc2006

Topics of Interest

Submissions are solicited on a wide variety of topics related to applied reconfigurable computing, including but not limited to:

- Methods and Tools (High-Level Compilers, Simulation, Estimation, Design space exploration, Languages to program reconfigurable systems, etc.)
- Architectures (Fine-grained, coarse-grained, and mixed-grained, Multi-processor-based reconfigurable platforms, Microprocessors with tightly-coupled reconfigurable hardware, etc.)
- Applications (High-Performance Systems, use of reconfigurable computing in embedded systems, robotics, digital signal processing, etc.)
- Teaching reconfigurable computing
- Surveys and Future Trends
- Benchmarks (papers presenting benchmarks publicly available to be used by the reconfigurable computing community are especially welcome)

Submissions

Authors are invited to submit original work in the following formats:

- **Full papers (12 pages maximum):** they should include mainly accomplished results;
- **Short papers (6 pages maximum):** they should be mostly composed of work in progress reports or fresh development;

Submissions should be sent as an e-mail attachment in pdf format to:

k.l.m.bertels@ewi.tudelft.nl

- In order to maintain a blind review no information about authors should be included in the papers being submitted
- The format of the papers should be according to LNCS format rules. Consult the website for more information.
- The email should include the title of the paper being submitted, the names and affiliations of the authors, and should identify if it is a full or short paper.

Evaluation

Each paper will be reviewed by at least three members of the program committee.

Important Dates

- **Submission deadline: 4 December, 2005**
- **Notification to authors: 7 January, 2006**
- **Camera-Ready Submission : 7 February 2006**
- **Registration: from 7 January, 2005**
- **Workshop: 1-2-3 March, 2006**

Publication

The proceedings will be published in the Springer Verlag LNCS series. The best papers of the workshop will be published in a special edition of the International Journal of Electronics (Taylor & Francis Group).

Please use the LLNCS.zip file on <http://www.springer.com/sgw/cda/frontpage/0,11855,5-164-2-72376-0,00.html>

for Latex instructions